

AMENDMENTS TO THE SPECIFICATION

Please amend the paragraph beginning on page 13, line 1 of the substitute specification filed on November 5, 2007 as follows:

A first embodiment of the present invention will be described with reference to figures 5 to 8. An error correction method according to the first embodiment is a method for performing error correction on interleaved data in an ECC block, as shown in figure 4. Therefore, initially error correction is performed on sub data, and erasure position information of main data is calculated on the basis of the result of error correction, and the information is used when performing error correction on the main data, as described for the conventional example. That is, the erasure position information is a parameter for tracking down an error in each code line of the main data. When the data in the ECC block has been subjected to Reed-Solomon coding, error position information is obtained from a position polynomial that is calculated at the time of performing Reed-Solomon decoding, and the error position information is calculated by using a specific algorithm to obtain erasure position information.